REMARKS

In the Office Action, claims 1-5 and 13-17 were rejected under 35 U.S.C. 103(1) as being unpatentable over Haumont et al. (U.S. Publication US 2001/0012279 A1 "Haumont") in view of Kim (U.S. Patent No. 6,052,713) and Farley et al. (U.S. Patent No. 6,553,032 "Farley"). Claims

5   6-7 were rejected under 35 U.S.C. 103(a) as being unpatentable over Haumont in view of Kim and Farley as applied to claims 1-5 and 13-17 and in further view of Strawczynski et al. (U.S. Application No. 09/835,102 "Strawczynski"). Claims 8, 11-12 and 18-19 were rejected under 35 U.S.C. 103(a) as being unpatentable over Haumont in view of Kim. Claims 9-10 were rejected under 35 U.S.C. 103(a) as being unpatentable over Haumont in view of Kim as applied to claims

10 8, 11-12, and 18-19, and further in view of Kumar et al. (U.S. Patent No. 6,507,572 "Kumar"). Claim 20 was rejected under 35 U.S.C. 103(a) as being unpatentable over Haumont in view of Kim as applied to claims 8, 11-12, and 18-19, and in further view of Strawczynski. Claim 1 is amended herein to correct a typographical error.

Haumont, which serves as the primary reference for all rejections and discloses a first

15 embodiment having distributed buffers in a plurality of base station. (see page 4, paragraph [0049]) Haumont further discloses a second embodiment having a central buffer located in a base station controller (see page 6, paragraph [0079]). Haumont fails to disclose a system having both a central buffer in the base station controller and distributed buffers in a plurality of base stations serviced by the base station controller. Haumont further fails to disclose, for each of an

20 active set of base stations, downloading a plurality of blocks of data of the group of blocks of data from a central buffer to a respective distributed buffer of the base station. Haumont also fails to disclose downloading a next plurality of blocks of data of the group of blocks of data from a central buffer to the respective distributed buffer of each base station of the active set of

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base stations. The Office Action concedes that Haumont, Farley, Strawczynski, Padovani, and Kumar fail to disclose the elements that relate to both a central buffer and a plurality of distributed buffers. The Office Action cites Kim as meeting the claim elements that these references fail to meet. Applicants respectfully disagree.

5   The background section of Kim (col. 1, lines 15-63), states that programs and data are downloaded from a main processor of a Base Station Manager (BSM) to a medium processor of a Base Station Controller (BSC) and from the medium processor of the BSC to sub processors of respective Base station Transceiver Subsystems (BTSs). The Office Action states that this section of Kim discloses the central buffer and distributed buffer of the claims of the present

10   invention. The Office Action infers teachings to Kim that Kim simply does not disclose. The teachings of prior art references must be evident and explicit, not inferred or missing as is the case here. The BSM described in the background section of Kim manages individually each sub processor. A more reasonable interpretation of the background section of Kim is that different data and programming would be transmitted by the main processor or medium processor to each

15   sub processor for assignment of resources.

The Office Action also cites Kim at col. 3, line 50, to col. 4, line 23, as teaching the central buffer/distributed buffers elements of claims 1, 8, 13, and 18. A careful reading of these portions of Kim shows that Kim addresses the **uploading** of data from the sub processors of the BTSs to the main processor of the BSS **not** the **downloading** of data from a central buffer to

20   distributed buffers. The summary of the invention of Kim (col. 2, lines 15-31) explicitly states that the objects of the invention relate to the receipt and storage of data from a plurality of sub processors by a main processor. Figures 2-5 of Kim and related text at col. 4, line 27 to col. 6, line, 12, describes how data from the sub processors (BTSs) is uploaded, processed, and stored

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by the single main processor (BSC). In particular, referring to FIG. 2, sub processors (211, 212,

213) that resident in respective BTSs transfer data to components of a BSC (control process 220,

buffering process 230, ready queue 260, storing process 240, and database 250).

    The claims of the present invention address methods and structure for servicing <u>forward</u>

5   <u>link transmissions for a mobile station</u>. For example, independent claim 1 is directed to a

"method of operating a wireless communication system to service <u>high data rate forward link</u>

<u>transmissions for a mobile station</u>." Kim fails to teach or suggest: (1) downloading a group of

blocks of data to a central buffer that services the active set of base stations; (2) for each of the

active set of base stations, downloading a plurality of blocks of data of the group of blocks of

10   data from the central buffer to a respective distributed buffer of the base station; and (3)

downloading a next plurality of blocks of data of the group of blocks of data from a central

buffer to the respective distributed buffer of each base station of the active set of base stations as

required by claim 1. Kim fails to meet the shortcomings of Haumont, Farley, Strawczynski,

Padovani, and Kumar. Therefore, none of the obviousness rejects are proper and al must be

15   withdrawn.

    Claims 8, 13, and 18 require similar interaction between the central buffer and the

plurality of distributed buffers. For these reasons, independent claims 1, 8, 13, and 18 are not

obvious over the cited references. All other pending claims depend from one of claims 1, 8, 13,

or 18 and are allowable for at least this reason. All claims are now allowable and a notice of

20   allowance is courteously solicited. Please direct any questions or comments to the undersigned

attorney at the address indicated.

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Respectfully submitted,

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